

(12) INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

(19) World Intellectual Property Organization  
International Bureau



(43) International Publication Date  
31 July 2003 (31.07.2003)

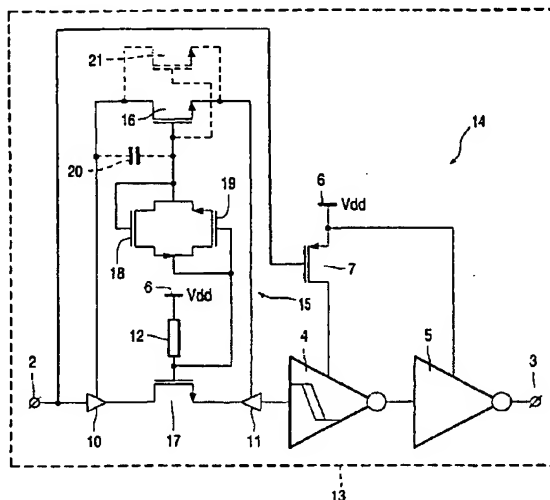
PCT

(10) International Publication Number  
WO 03/063198 A2

- (51) International Patent Classification<sup>7</sup>: H01L (74) Agent: DUIJVESTIJN, Adrianus, J.; Internationaal Octrooibureau B.V., Prof. Holstlaan 6, NL-5656 AA Eindhoven (NL).
- (21) International Application Number: PCT/IB02/05484
- (22) International Filing Date:  
12 December 2002 (12.12.2002) (81) Designated States (*national*): AE, AG, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BY, BZ, CA, CH, CN, CO, CR, CU, CZ, DE, DK, DM, DZ, EC, EE, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, MZ, NO, NZ, OM, PH, PL, PT, RO, RU, SC, SD, SE, SG, SK, SL, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VC, VN, YU, ZA, ZM, ZW.
- (25) Filing Language: English
- (26) Publication Language: English
- (30) Priority Data:  
02075258.0 22 January 2002 (22.01.2002) EP
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- (72) Inventor; and (84) Designated States (*regional*): ARIPO patent (GH, GM, KE, LS, MW, MZ, SD, SL, SZ, TZ, UG, ZM, ZW), Eurasian patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European patent (AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, IE, IT, LU, MC, NL, PT, SE, SI, SK, TR), OAPI patent (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG).
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[Continued on next page]

(54) Title: A VOLTAGE LIMITING SEMICONDUCTOR PASS GATE CIRCUIT



(57) Abstract: A voltage limiting semiconductor pass gate circuit (15), comprises a first transistor (16), operatively connected to an input node (10) and an output node (11) of the pass gate circuit (15), and a second transistor (17), operatively connected between the input node (10) and the output node (11). The second transistor (17) has a control electrode biased to a supply voltage (6), and the first transistor (16) has a control electrode which connects by two back-to-back connected diode elements (18, 19) to the control electrode of the second transistor (17). The pass gate circuit (15) is typically applied in input I/O cells (14) of semiconductor integrated circuits (13).

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**Published:**

— without international search report and to be republished  
upon receipt of that report

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## INTERNATIONAL SEARCH REPORT

PCT/IS 02/05484

A. CLASSIFICATION OF SUBJECT MATTER  
IPC 7 H03K17/06

According to International Patent Classification (IPC) or to both national classification and IPC

## B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)  
IPC 7 H03K

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the International search (name of data base and, where practical, search terms used)

PAJ, EPO-Internal, WPI Data

## C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	JP 60 177714 A (HITACHI SEISAKUSHO KK) 11 September 1985 (1985-09-11)	1-3
Y	figure 1	4-8
Y	US 6 271 703 B1 (WERT JOSEPH DOUGLAS) 7 August 2001 (2001-08-07)	4-8
A	column 5, line 24-43; figure 4	1-3

☐ Further documents are listed in the continuation of box C.

☒ Patent family members are listed in annex.

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Date of the actual completion of the international search

27 January 2004

Date of mailing of the international search report

09/02/2004

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## INTERNATIONAL SEARCH REPORT

PCT/IB 02/05484

Patent document cited in search report	Publication date	Patent family member(s)	Publication date
JP 60177714	A	11-09-1985	NONE
US 6271703	B1	07-08-2001	NONE